

# Thermal phase shifters for femtosecond laser written photonic integrated circuits

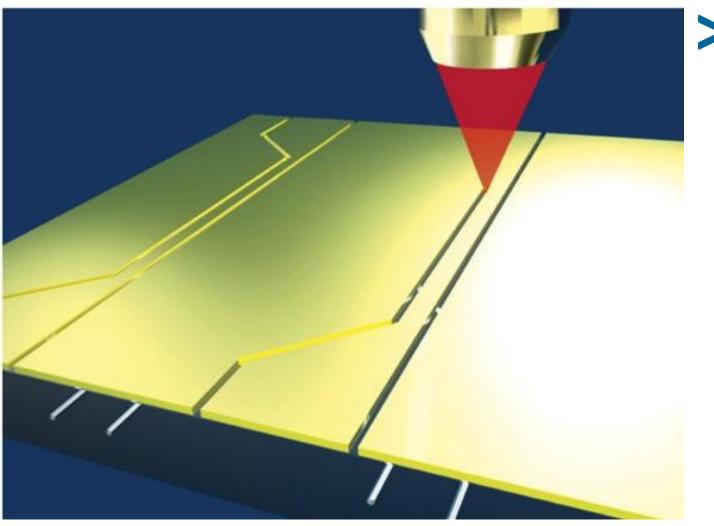
Ciro Pentangelo<sup>b,a</sup>, Emanuele Urbinati<sup>b,a</sup>, Simone Atzeni<sup>b,a</sup>, Francesco Ceccarelli<sup>a,b</sup>, Roberto Osellame<sup>a,b</sup>

<sup>a</sup> Istituto di Fotonica e Nanotecnologie – Consiglio Nazionale delle Ricerche (IFN–CNR), piazza Leonardo da Vinci 32, 20133 Milano, Italy. <sup>b</sup> Dipartimento di Fisica – Politecnico di Milano, piazza Leonardo da Vinci 32, 20133 Milano, Italy.

Compared to bulk and fiber optic systems, photonic integrated circuits (PICs) offer improved performance in terms of scalability, compactness and stability at a lower cost. Femtosecond laser micromachining (FLM) is a promising technology for the rapid and costeffective manufacturing of PICs. Reconfigurability in FLM circuits can be achieved with thermal phase shifters (i.e. resistive microheaters), fabricated by depositing a gold film on top of the substrate and ablating it with a femtosecond laser. Here, we exploit this technology to demonstrate a 6-mode universal photonic processor featuring 30 thermal shifters isolated by 60 deep trenches. However, this kind of thermal shifters is severely limited in terms of integration density and, for this reason, we are currently investigating a lithographic fabrication process in PoliFAB. Being spin coating of liquid photoresist not easily compatible with deep isolation trenches, we are now developing a new fabrication process based on dry film photoresist lithography to overcome the limits of gold-based thermal shifters while retaining the advantages of thermal isolation.

## Gold thermal phase shifters via FLM

# Lithographic thermal phase shifters



### **> FABRICATION PROCESS**

- Thermal evaporation of gold film
- Thermal annealing
- FLM ablation of resistors

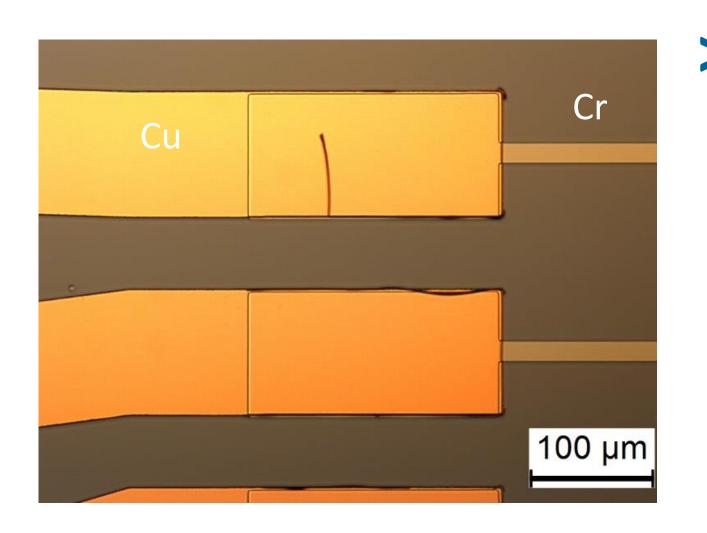
Thermal annealing a

ensures improved stability

and reliability



Thermal isolation structures such as deep trenches significantly reduce power dissipation and thermal crosstalk



#### > DESIGN

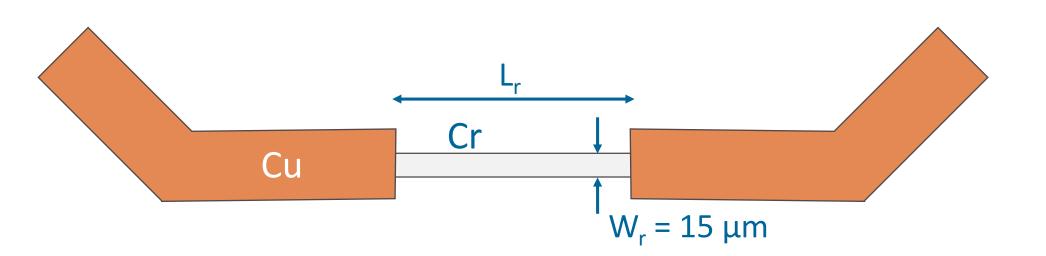
Chromium resistor:

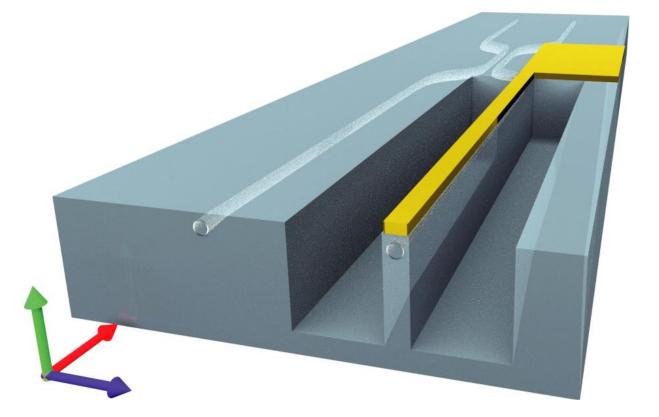
- good adhesion to glass
- good thermal stability

**Copper interconnections:** 

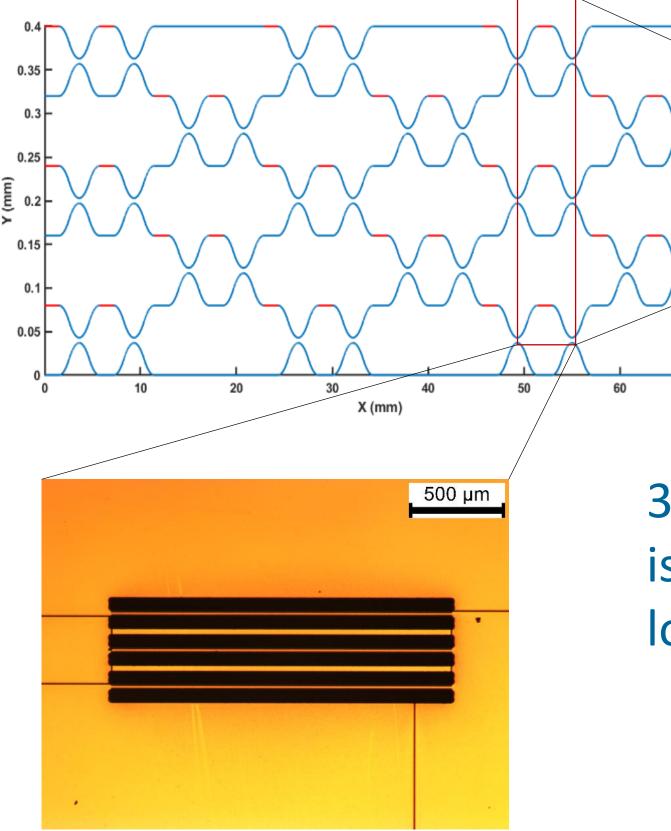
- wire-bonding compatible
- low resistivity

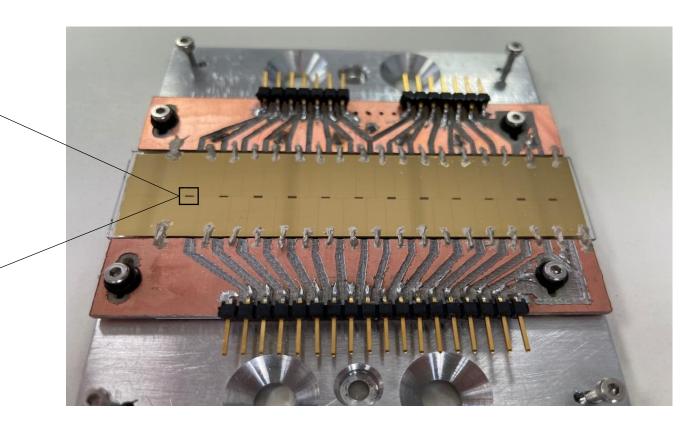
Chromium and copper are deposited by E-beam evaporation in two separate lift-off lithography steps.



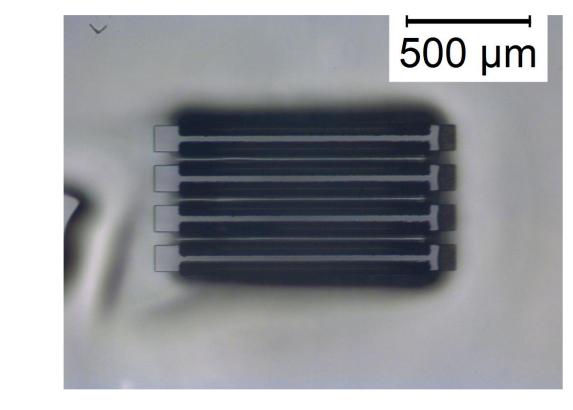


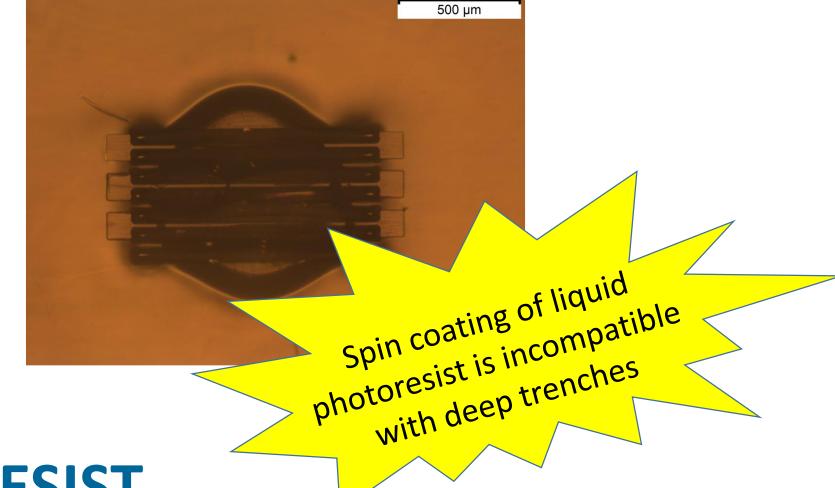
#### > 6 MODE UNIVERSAL PHOTONIC PROCESSOR





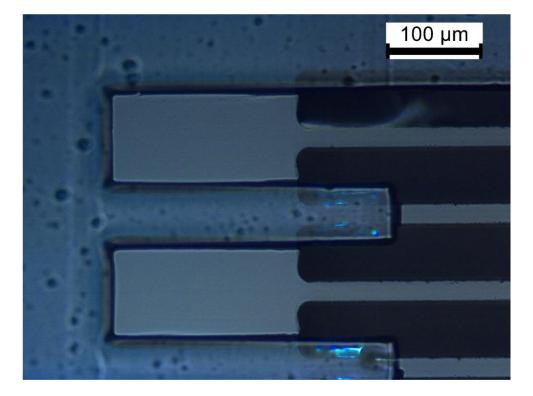
microheaters featuring deep 30 isolation trenches on the same 9 cm long device Upper limit for integration of gold thermal shifters



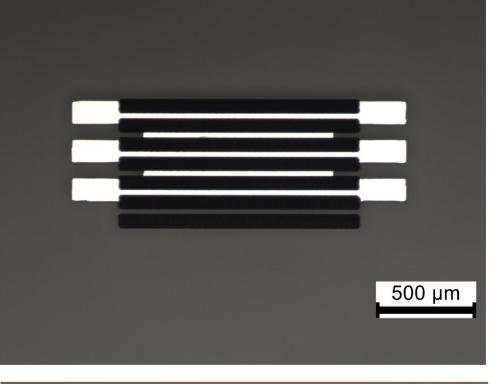


# > DRY FILM PHOTORESIST

Coating by lamination of thick negative Ordyl FP450 resist

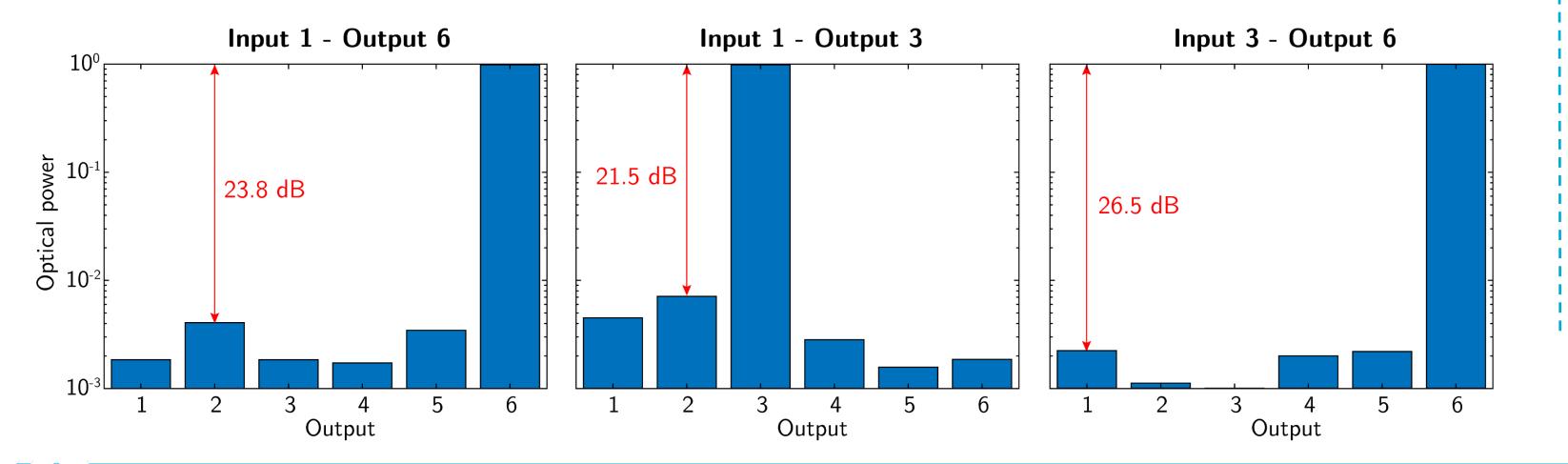


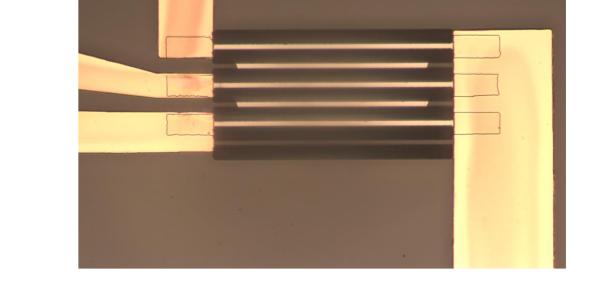


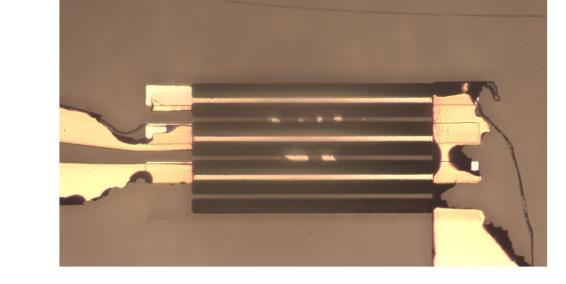


500 µm

#### > FIRST APPLICATION: PHOTONIC SWITCHING







Second lift-off of thick copper presents some tearing issues limiting yield. Optimization in progress...

#### References

[1] Flamini et al. *Light: Science & Applications* **4**(11), e354 (2015). [2] Ceccarelli et al. Journal of Lightwave Technology **37**(17), 4275-4281 (2019). [3] Ceccarelli et al. *Laser & Photonics Reviews* **14**(10), 2000024 (2020). [4] Pentangelo et al. *Physical Review Research* **3**(2), 023094 (2021).



Roberto Osellame roberto.osellame@cnr.it https://www.capable-erc.eu/ https://www.phoqusing.eu/



